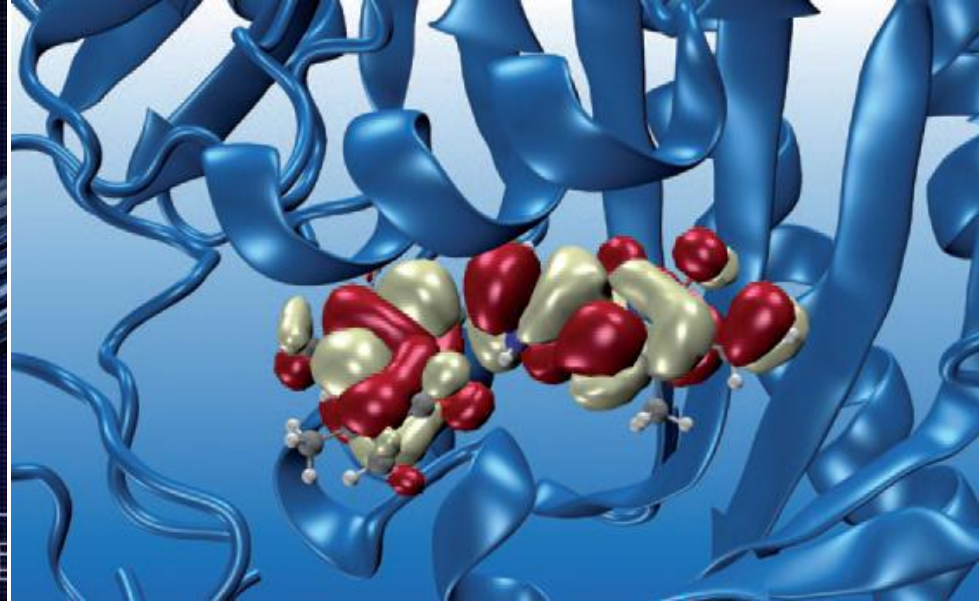


Who can beat X86?

Piero Altoè

Workstation . Server . Storage & Solutions



E4® Computer Engineering S.p.A. specializes in the manufacturing of high performance IT systems of medium and high range. Our products aim to accomplish both industrial and scientific research requirements and space from universities to computing centers.

Thanks to the established experience and quality in this circle, E4® has become a valued technology's supplier and it is acknowledged and appreciated by famous and prestigious organizations such as C.E.R.N. in Geneva.



HARDWARE AGNOSTICS

Our “hardware agnostic” approach allows us to provide highly personalized solutions of outstanding quality, which adhere to the customer’s needs without favoring one technology over another.

We offer state of the art informatics at the best possible price/performance ratio and we even give the opportunity to test the system from remote or in loco, so as to further help the customer in choosing the right product and making a thoughtful purchase free from unexpected events.



E4® PRODUCT RANGE

Our product range is as diverse as our customer base

- Workstation for scientific/engineering applications (CAD, CAE, CFD, FEM, etc.)
- Workstation for professional editing
- Computing Server
- Virtualization Server data center/ISP
- Cluster Server in high availability
- Storage server/NAS
- Storage/SAN in high availability
- HPC storage
- HPC cluster
- GPU cluster
- Advanced visualization systems (stereoscopy, 3D cave ...)
- Data centers

R&D

Our R&D department is made of engineers specialized in hardware, software and applications. Research focuses on intense studies of the most innovative technologies so as to increase the Company's technical/scientific knowledge. With the acquired know-how we develop and create prototypes, then the first pilot series up to the finished product, ready to be manufactured.



Our staff's knowledge and efficiency, paired to their daily relationship with the main manufacturers of technology, allow E4® to offer ever innovative products with the best Time to Market available, to ensure the effectiveness of our customers' IT investments.



CERTIFICATIONS

- *ISO 9001:2008*
- *ISO 14001:2004*
- *Intel Platinum Technology Provider*
- *Tesla Preferred Provider*
- *AMD Platinum Premier*
- *VMware – professional solution provider*
- *Microsoft Gold certified*
- *Intel Cluster Ready*



CORPORATE SOCIAL RESPONSIBILITY

Definition from EU is : «The responsibility of enterprises for their impacts on society»

For E4, which has always been committed in this area, being socially responsible means not only fulfilling legal obligations, but also going beyond compliance and investing in human capital, in relations with stakeholders and sustainable development.



case history



CERN – Geneva (Switzerland)

5.357 dual socket mainboards

68.476 cores

34.300 server grade hard disks

67PB

The image is a promotional graphic for E4 Computer Engineering S.p.A. It features the INFN logo (Istituto Nazionale di Fisica Nucleare) in the top left. The main text is in Italian, celebrating the company's industrial collaboration excellence for the ATLAS and CMS experiments at the CERN Large Hadron Collider, specifically mentioning the discovery of the Higgs boson. On the right, there is a circular visualization of a particle collision with many tracks radiating from a central point. At the bottom left, there is a blue, elongated shape representing a particle detector component, also with tracks. The company name 'E4 Computer Engineering S.p.A.' and location 'Scandiano (RE)' are prominently displayed in the center. Below this, it states the company provided high-level trigger processors and T1/T2 calculation systems for the ATLAS and CMS experiments. Logos for CMS and ATLAS are at the bottom.

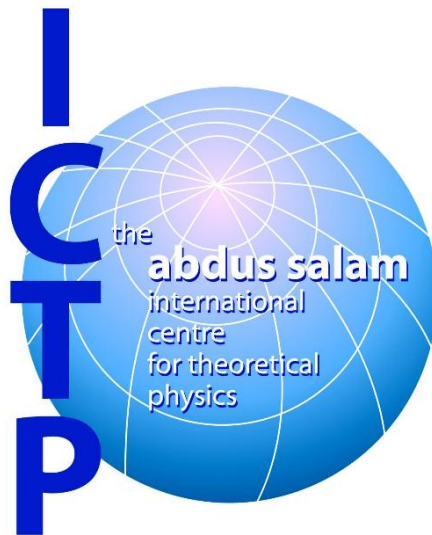
Ref: Olof Barring, Fatima Najeh



- > 12PB HPC storage (DDN with GPFS)
- > 1.000 server dual socket (~ 10k computing cores)
- Several GPU systems
- 4h intervention times

- > 5PB direct attached storage (Alice – CMS)
- > 3.500 server dual socket (~ 30k computing cores)
- Several GPU systems
- NBD intervention times

Ref: Mauro Morandin, Gianpaolo Carlino, Alessandro De Salvo



ICTP - Trieste (Italy)

High Performance Computing Cluster:

212 dual socket server
> 1.500 computing cores
Infiniband Host Channel adapter QDR 40Gbps
Infiniband Switch QDR 40Gbps
Networking
E4 HPC Suite

Ref: Clement Onime, Ulrich Singe

ENEA Italian National Agency for New Technologies, Energy and Sustainable Economic Development - Roma (Italy)



High Performance Computing Cluster (Cresco 4):

256 dual socket server
4.096 computing cores
Infiniband Host Channel adapter QDR 40Gbps
Infiniband Switch 336 QDR 40Gbps ports
Networking
E4 HPC Suite

Ref: Giovanni Bracco, Silvio Migliori

delivery scheduled for Sept. '13



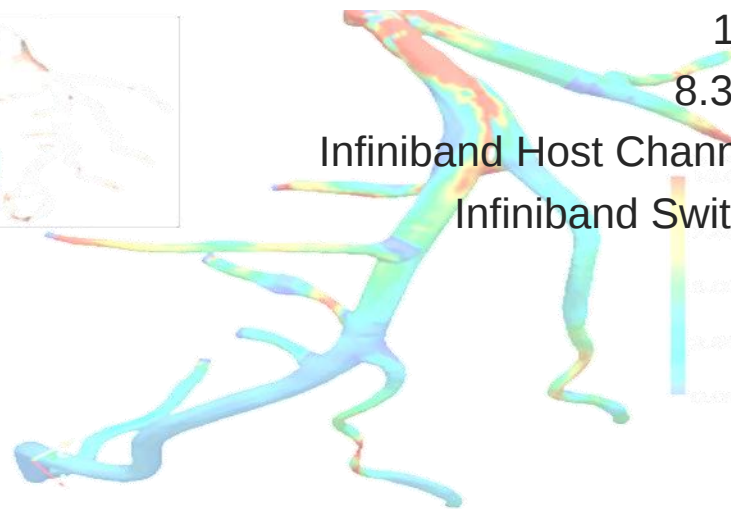
ÉCOLE POLYTECHNIQUE
FÉDÉRALE DE LAUSANNE



Prof. Efthimios Kaxiras

EPFL - Lausanne (Switzerland)

High Performance Computing Hybrid (GPU/CPU)
Cluster:



192 CPU computing cores

8.320 GPU computing cores

Infiniband Host Channel adapter QDR 40 Gbps

Infiniband Switch 36 QDR 40Gbps ports

Networking

E4 HPC Suite

Ref: Simone Melchionna



Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

ETH - Zurich (Switzerland)

HPC Hybrid (GPU/CPU) Cluster:



432 CPU computing cores
16.128 GPU computing cores
Infiniband Host Channel adapter QDR 40 Gbps
Infiniband Switch 432 QDR 40Gbps ports
1PB Lustre Storage (sustained 20Gb/s)
180 TB Panasas Storage

Ref: Olivier Byrde, Teodoro Brasacchio



PARTNERING
FOR MARITIME
INNOVATION

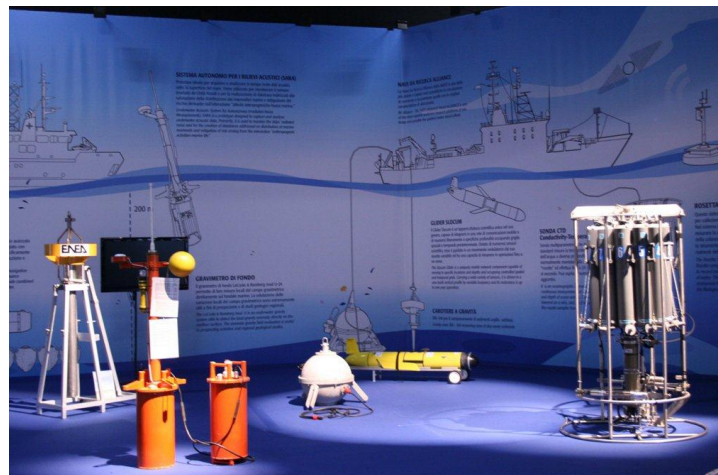
Nato Undersea Research Center - La Spezia (Italy)

Computing Server & HPC Storage:

> 1.000 CPU computing cores

>10.000 GPU computing cores

HPC Storage Panasas > 100TB



Ref: Francesco Baralli

European Synchrotron Radiation Facility - Grenoble (France)

Servers for X-ray beam data acquisition



Ref: Pierre Pinel



**Istituto
Europeo
di Oncologia**

Istituto di Ricovero e Cura
a Carattere Scientifico



European Institute of Oncology – Milano (Italy)

> 500TB ZFS Storage

Ref: Alessandro Della Vedova





Novartis - Siena (Italy)

High Availability HPC Cluster (Genomics applications):

96 dual socket server

1.152 computing cores

Infiniband Host Channel adapter QDR 40Gbps

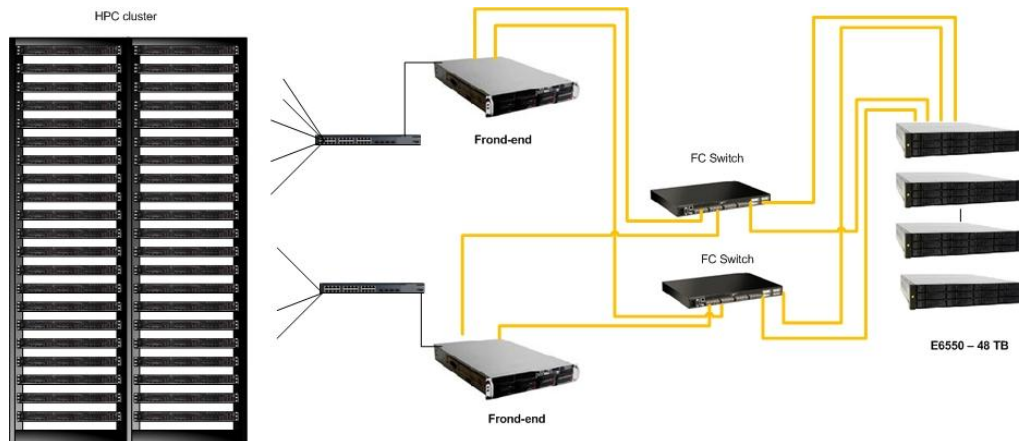
Infiniband Switch QDR 40Gbps ports

HPC Storage Panasas > 400TB

Networking

E4 HPC Suite

Ref: Riccardo Beltrami





E4 Solutions

WE ARE NOT GREEN

A simulation of 200 ns with 90 k atoms = 1week on 512 cores = 3200 kWh

Correspond to:

a) 1600 CO₂ kg

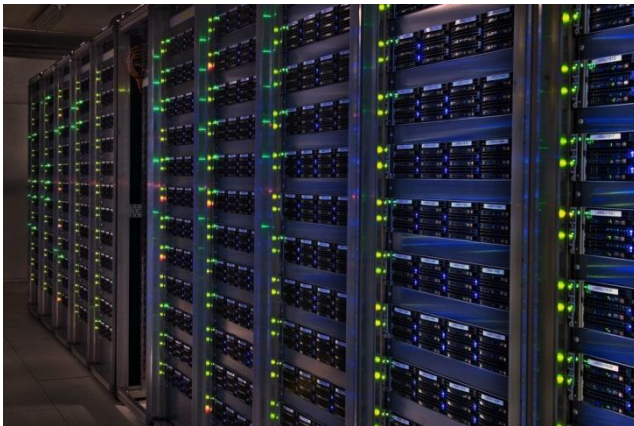
b) 340 € energy bill

c) 13000 km by car



WE ARE NOT GREEN

A NATIONAL SUPERCOMPUTING FACILITY HAS A YEARLY CO₂
FOOTPRINT COMPARABLE TO A TAKE OFF OF A SATURN V



=



Partnership



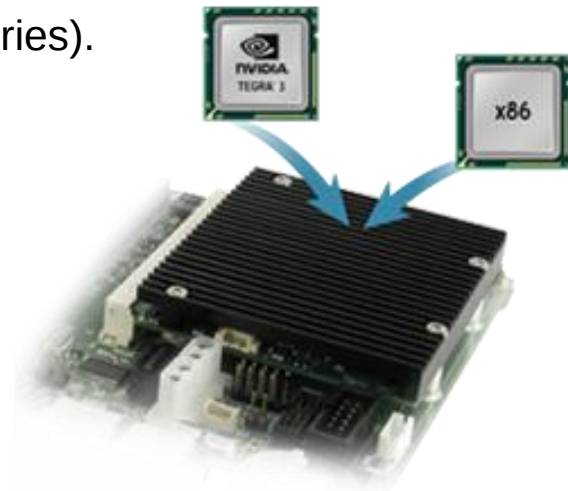
Established in 2002, E4 Computer Engineering designs and manufactures high performance systems which aim to accomplish both industrial and scientific research requirements and to reach a variety of customers ranging from universities to computing centers. E4's focus is on HPC although our expertise extends to all segments of IT.

SECO is an European designer and manufacturer of high integrated board computers and systems for embedded applications. Founded in 1979 in Italy, SECO attention is focused on developing innovative products with high performances, efficiency, low power consumption and increased functionality, offering in the meantime a short time-to-market.

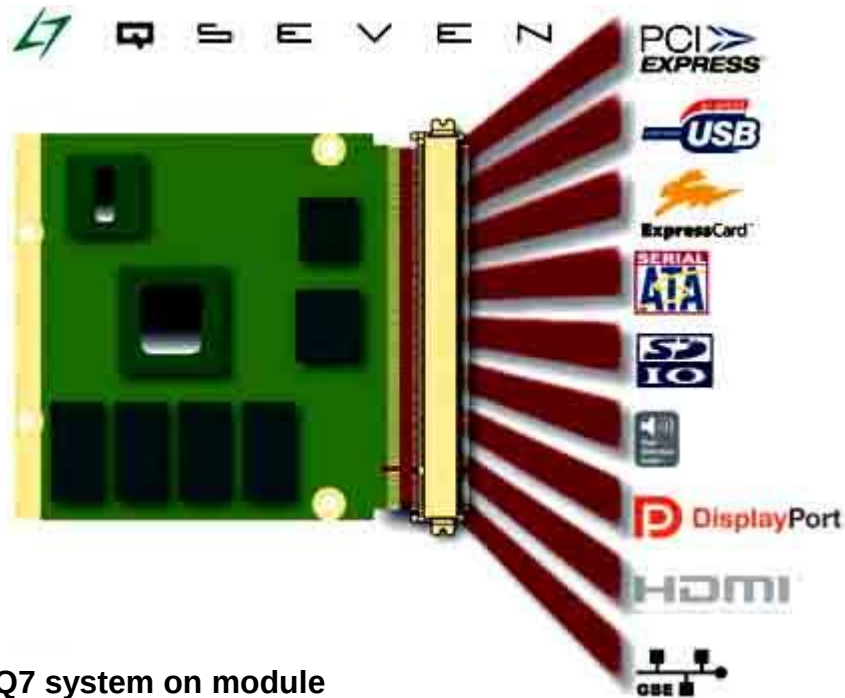
Modular Embedded Server (MES)

A MES is a stripped down server computer based on SoM optimized to minimize the use of physical space and energy.

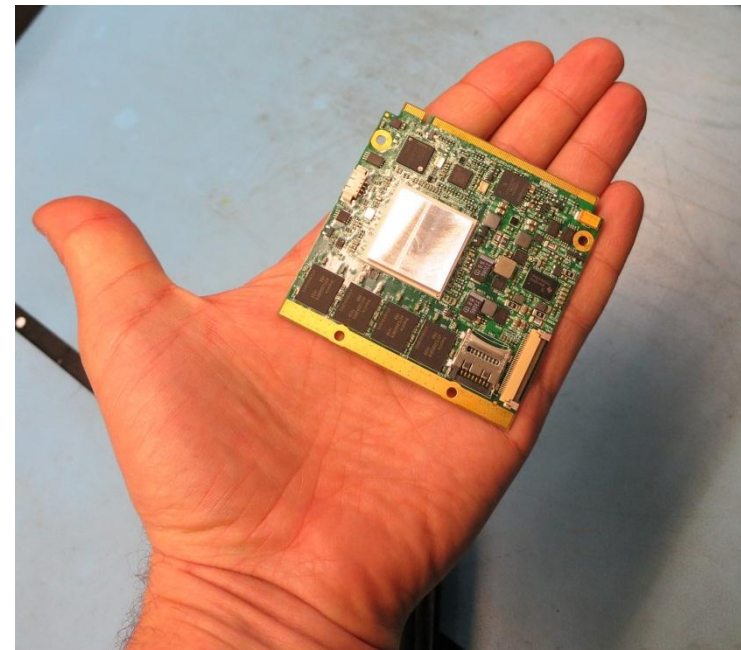
- Embedded CPUs, memory, controllers,...
- Equipped with low power CPUs (ARM, ATOM, G-Series).
- Based on System on Module (SoM)
- Easy to maintain



General purpose, power-efficient



Q7 system on module



ARKA MicroCluster Blade



Features	ARKA Blade
CPU	NVIDIA® Tegra® 3 ARM Cortex A9 Quad-Core
GPU	NVIDIA Quadro® 1000M with 96 CUDA Cores
Memory	2GB x CPU 2GB x GPU
Peak Performance	270 Single Precision GFLOPS
Network	1x Gigabit Ethernet
Storage	1x SATA 2.0 Connector
USB	3x USB 2.0
Display	3x HDMI (serial console available)



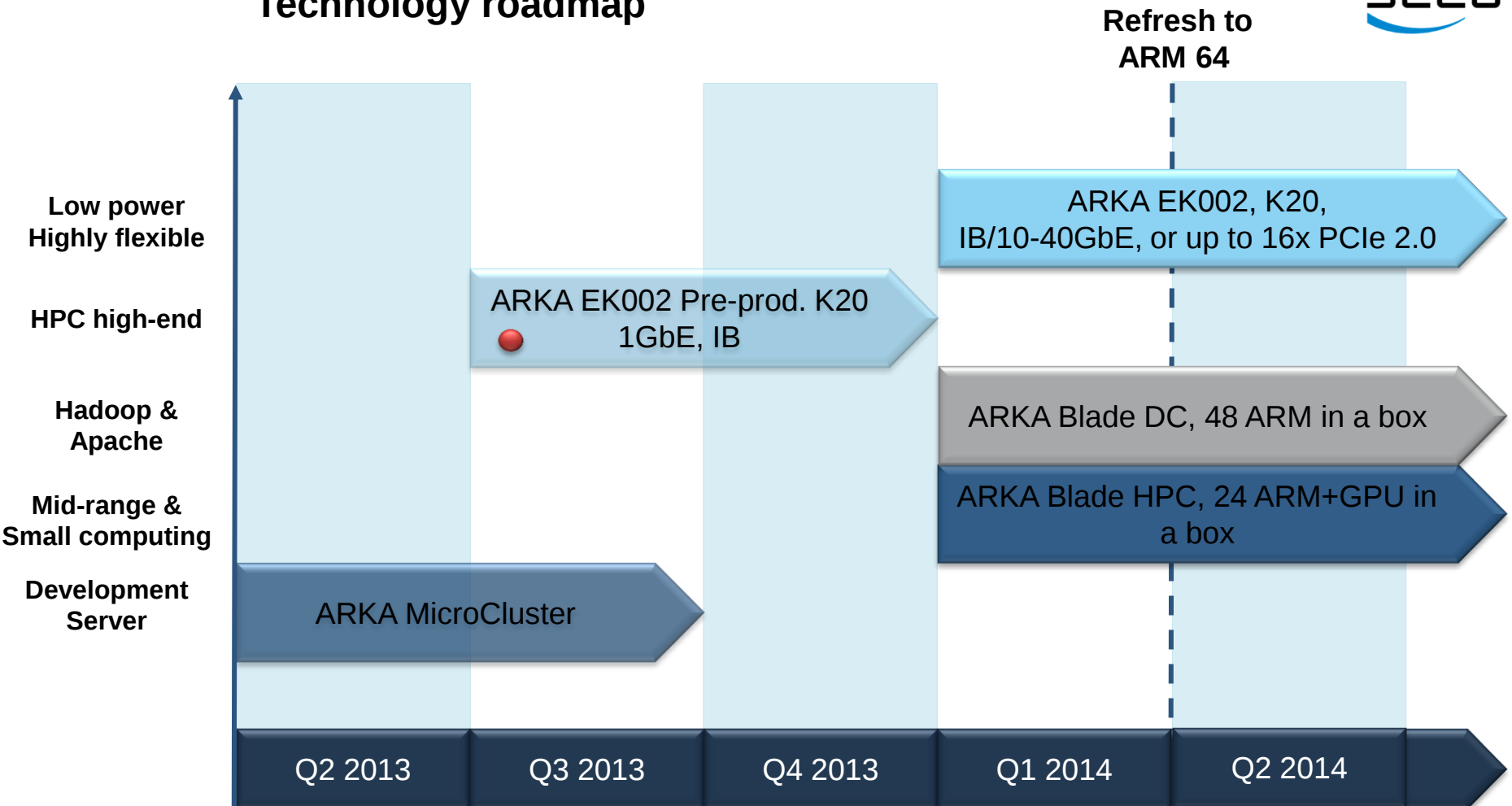
based on CARMA Devkit
developed by **SECO**

ARKA SHOC Test

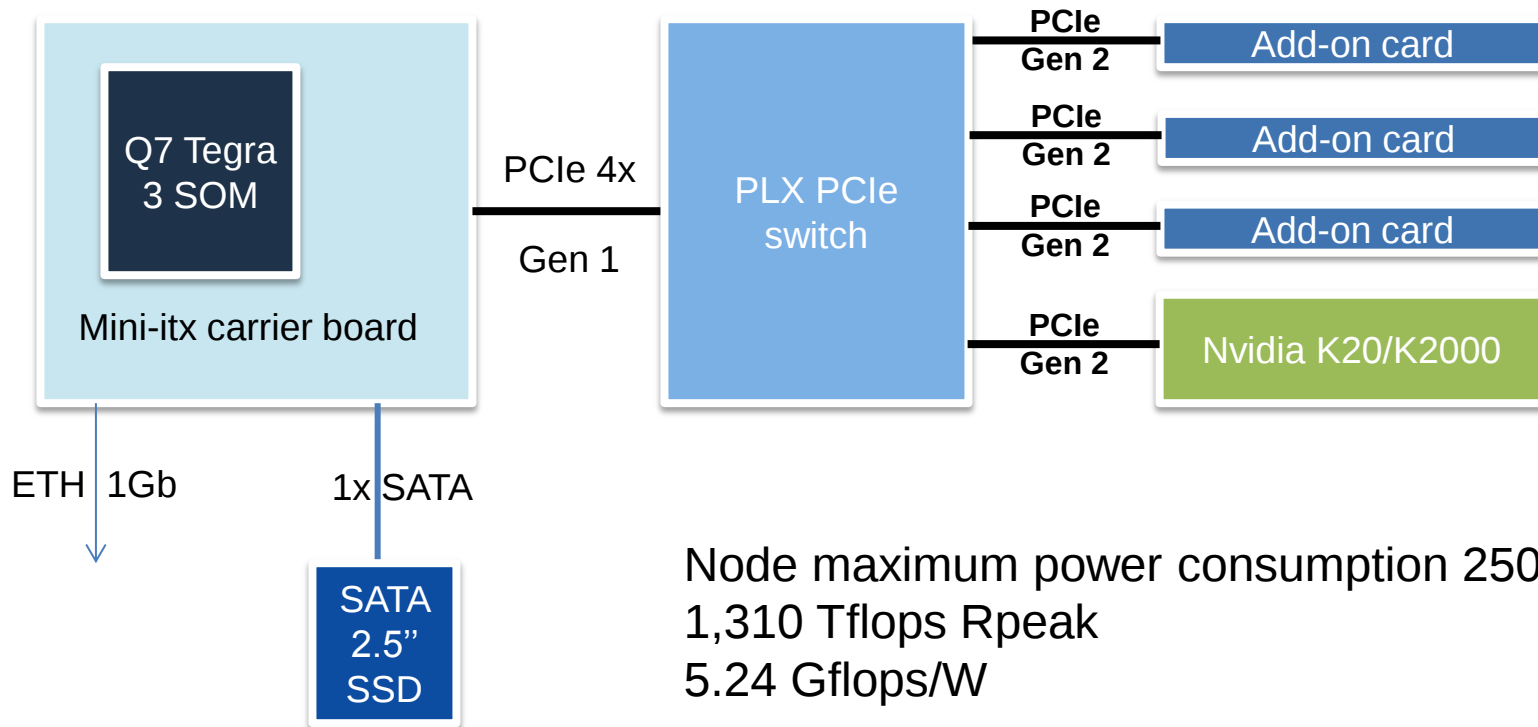
The Scalable Heterogeneous Computing Benchmark Suite (SHOC) is a collection of benchmark programs testing performance and stability (using CUDA and/or openCL)

Test	ARKA	Intel+M2075	Units	ARKA/M2075
Maxspflops	263.12	1001.31	GFlops	26%
fft_sp	23.343	162.524	GFlops	14%
sgemm_n	132.482	666.272	GFlops	20%
dgemm_n	21.5152	315.110	GFlops	7%
md_sp_bw	5.5301	25.3975	GB/s	22%
Reduction	23.1895	92.7343	GB/s	25%
Sort	0.3090	1.6296	GB/s	19%
triad_bw	0.4279	6.0163	GB/s	7%

Technology roadmap



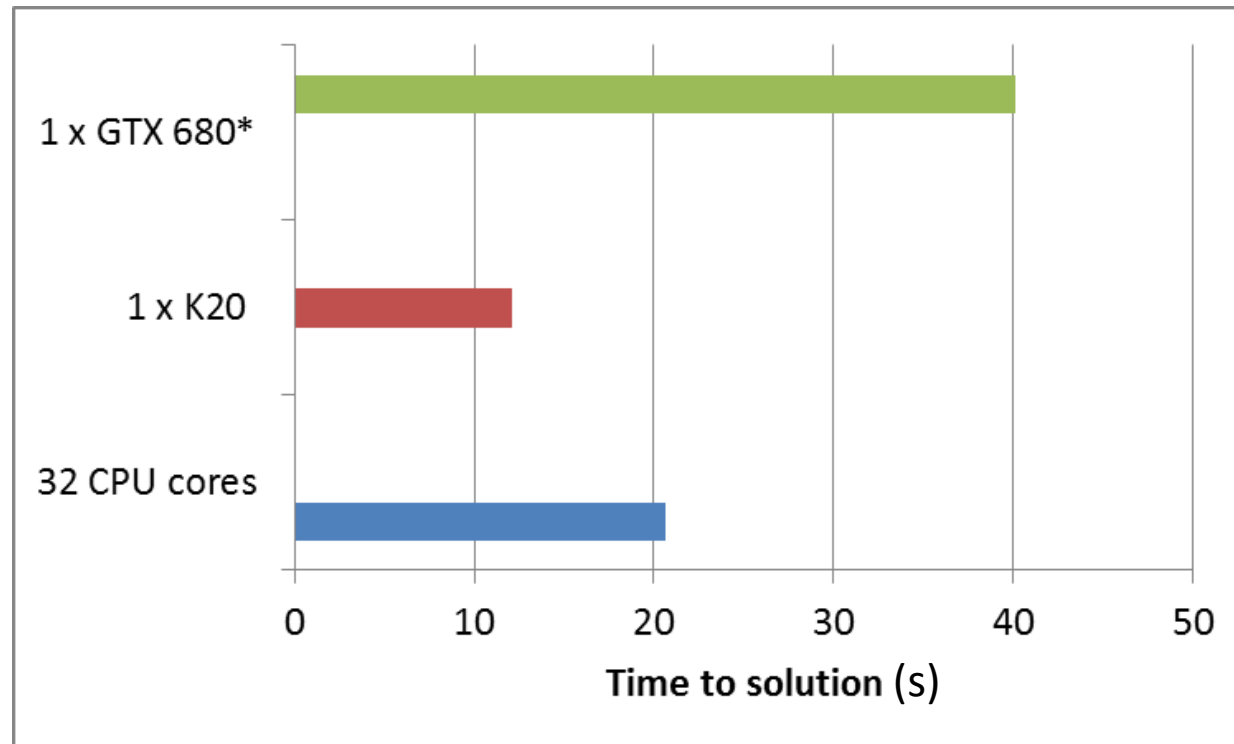
ARKA EK002 diagram



Node maximum power consumption 250W
1,310 Tflops Rpeak
5.24 Gflops/W



LAMMPS



***run in a ARM+GPU environment**

BSC PEDRAFORCA: first EK002 installation

Computing node:

74 or more pre-production EK002 server: ARM T30, K20, IB QDR

OS

Ubuntu derivative for ARM

Development tool

Cluster, monitoring, management tools

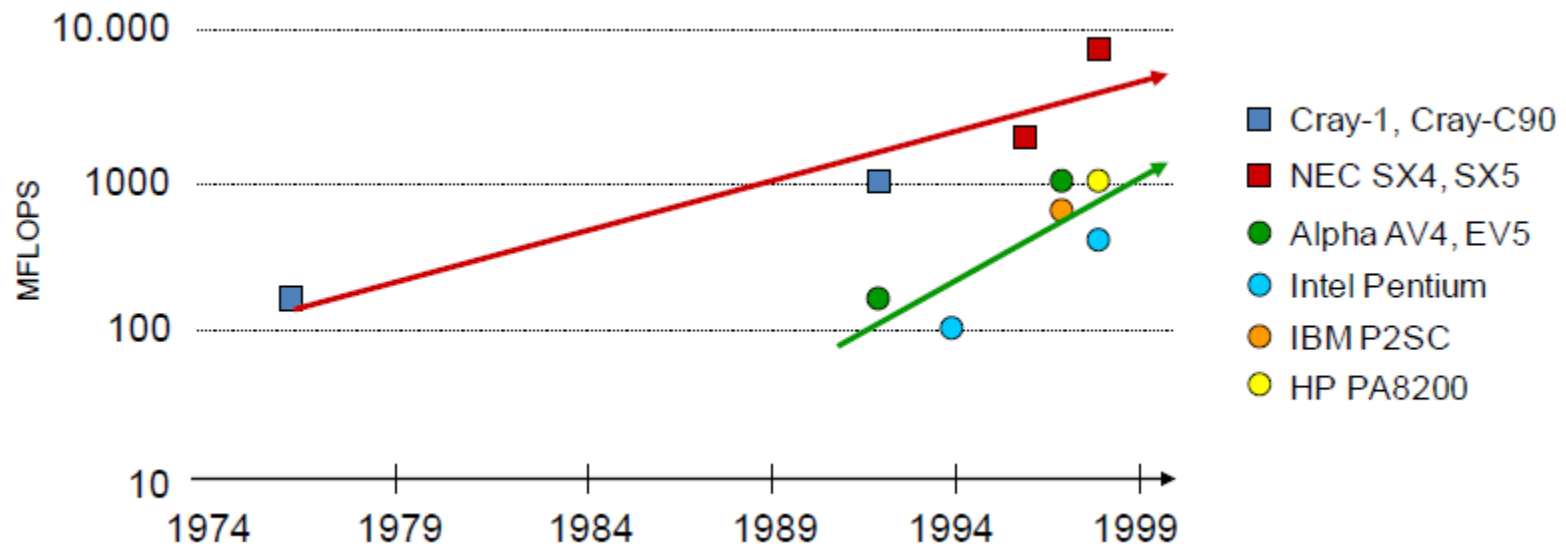
FP7- Project

<http://www.montblanc-project.eu/>



Pedraforca 2,506.4 m, Catalonia, Spain

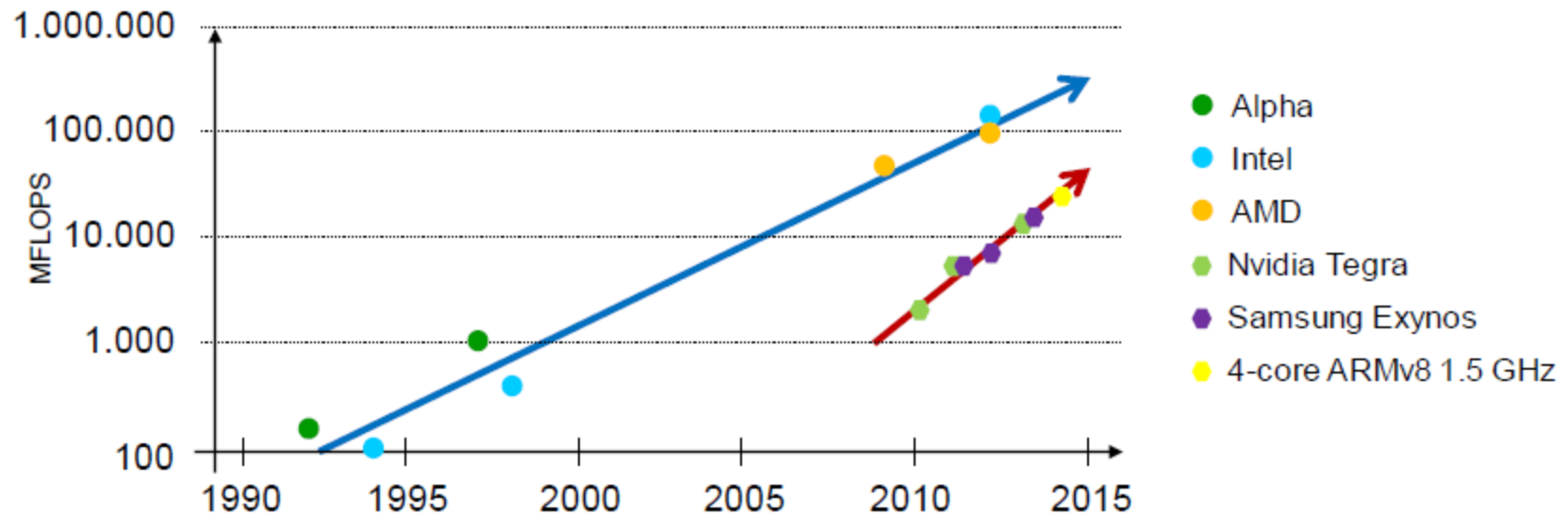
Past Killer Microprocessor



History may be about to repeat itself ...

- Mobile processor are not faster ...
- ... but they are significantly cheaper and greener

Present Killer Microprocessor



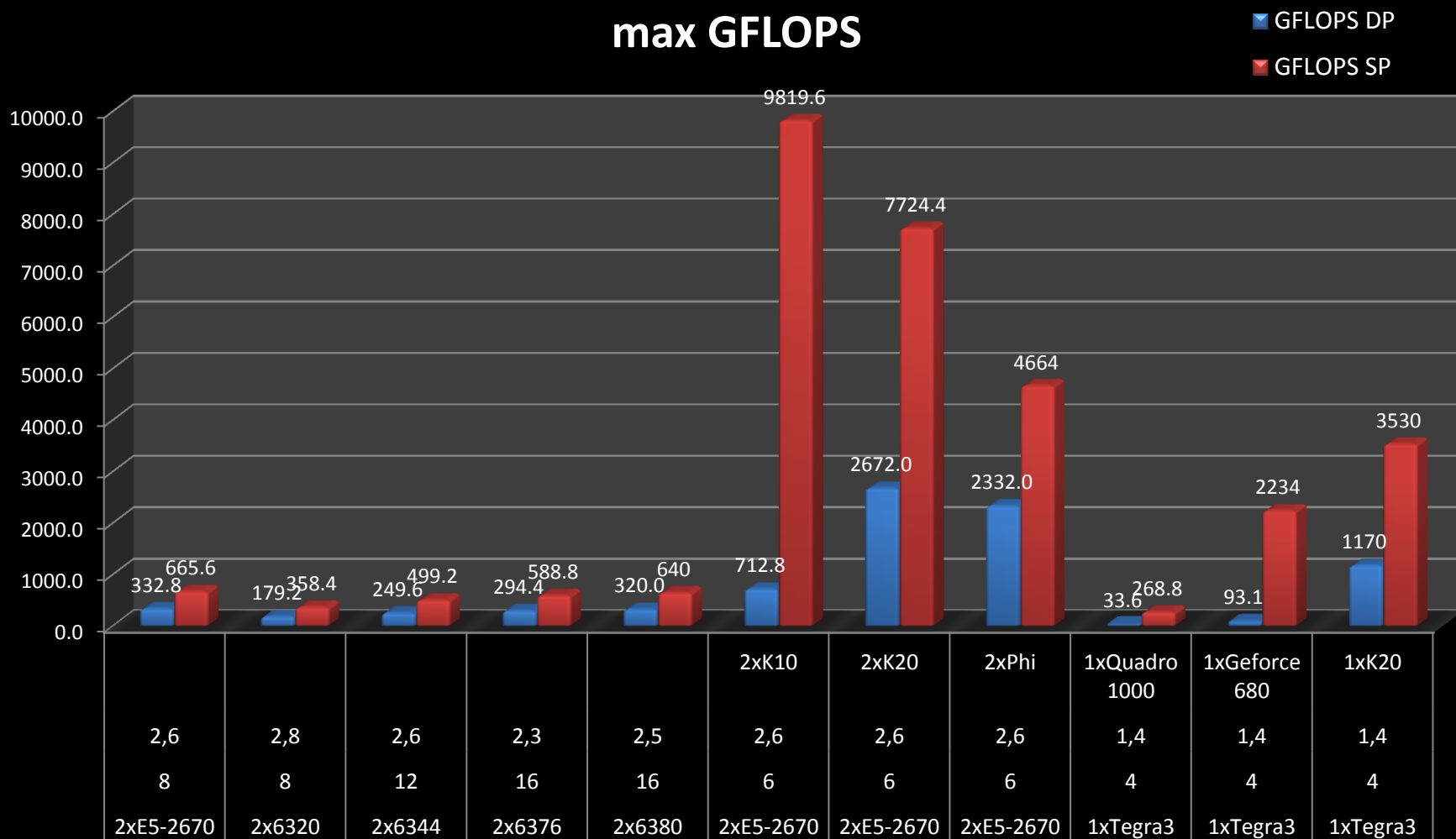
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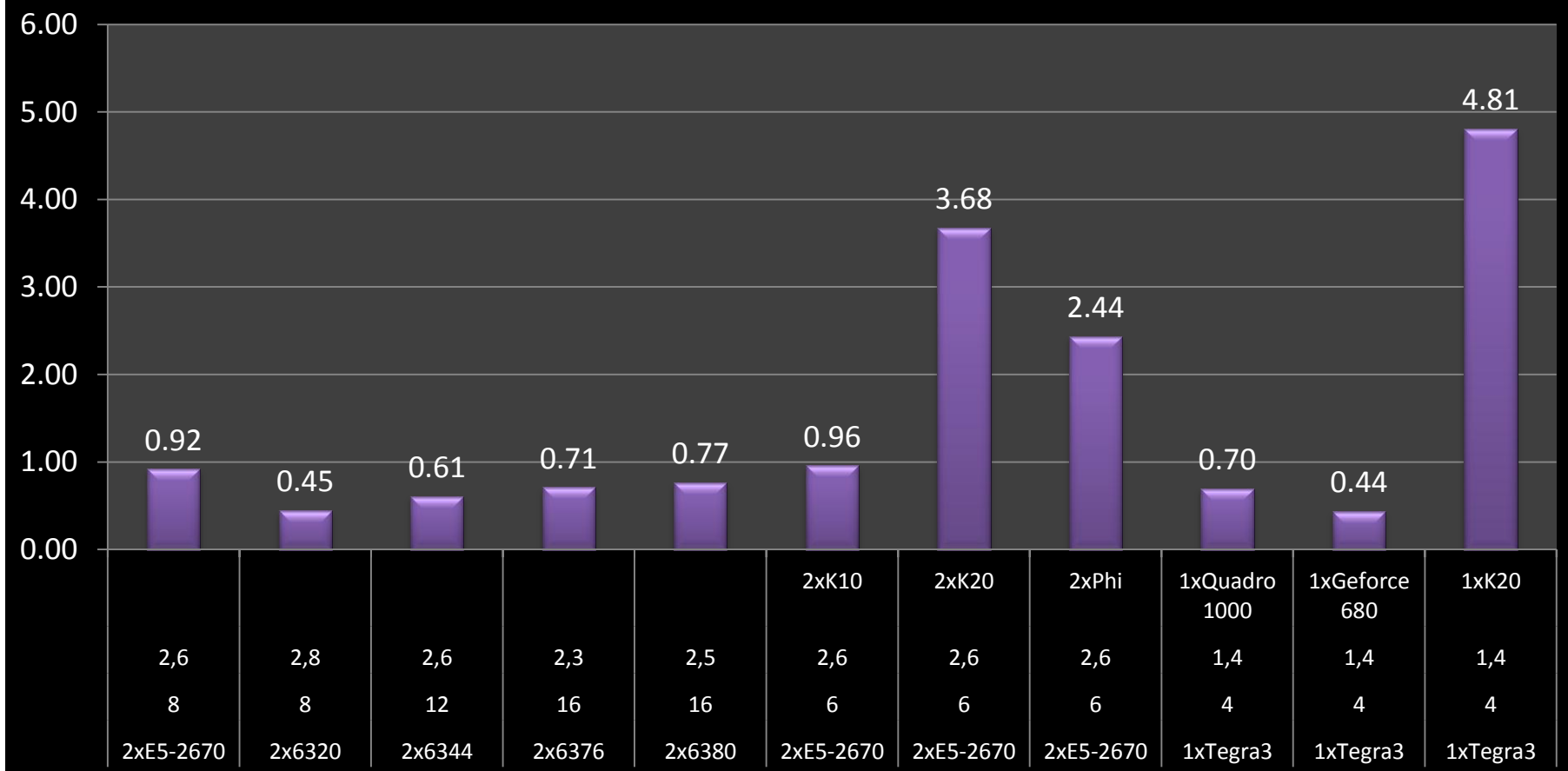


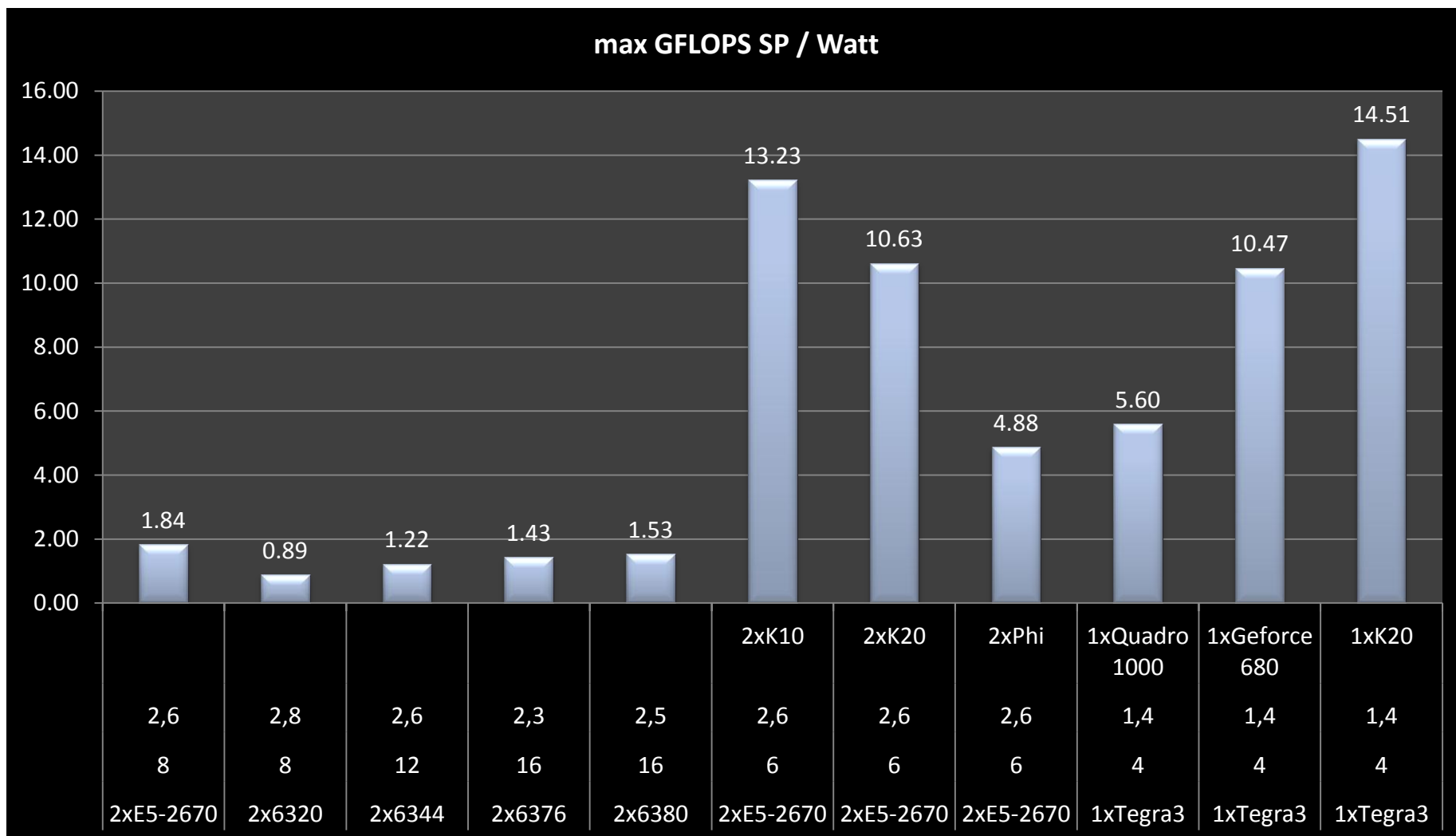
Benchmarks

max GFLOPS



max GFLOPS DP / Watt





max GFLOPS DP / k euro

■ max GFLOPS DP / k euro
■ including 3yrs power bill

